

CORNING

Advanced RF MEMS Switch Devices using Glass Packaging

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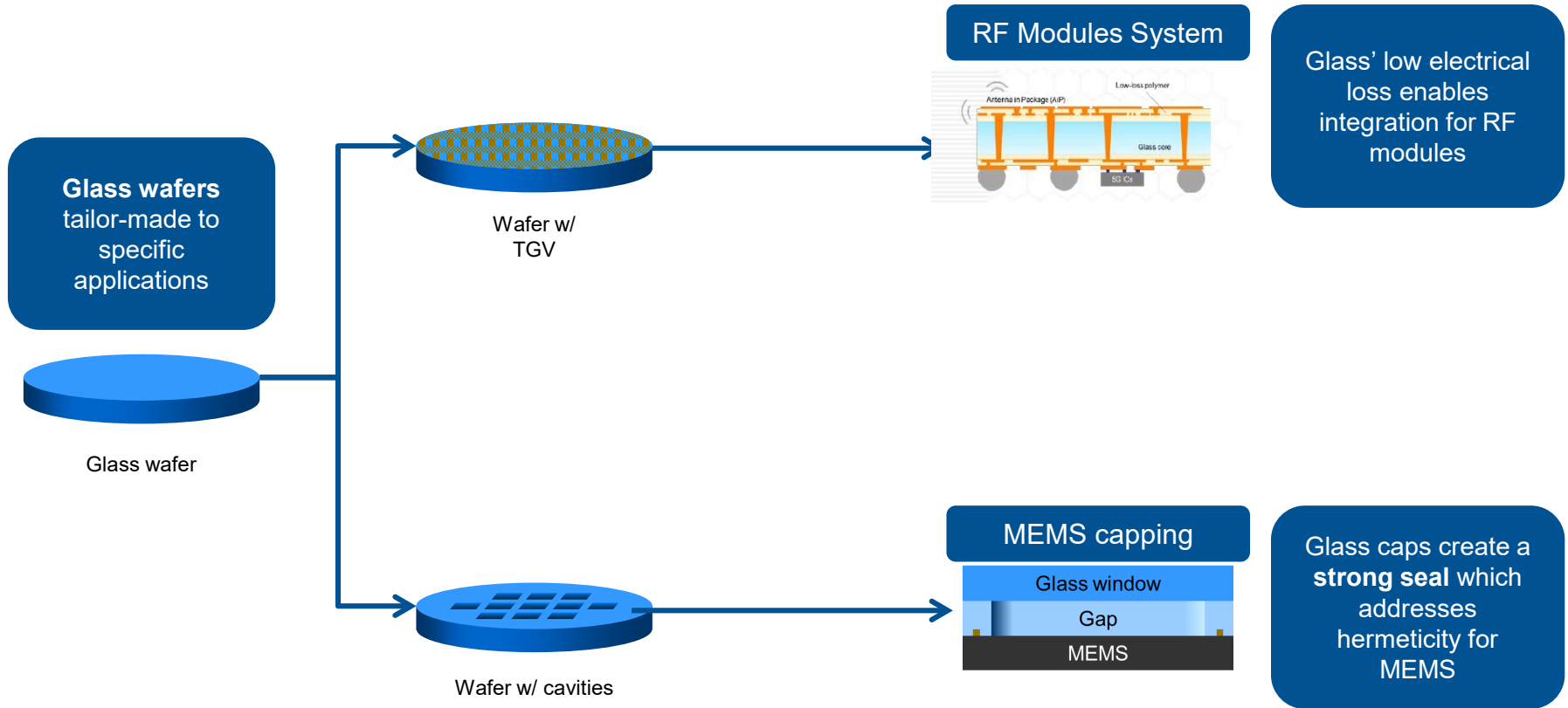
Outline

- **Development of Glass as a Substrate and Packaging Material**
 - **Material properties**
 - **Supply Chain**
- Evolution for Radio Frequency Front Ends
- RF Applications
 - LC-based Filters
 - MEMS Switches

The inherent properties of glass make it an excellent material of choice for RF and MEMS

		MEMS and RF		
		Packaging	As a Carrier	MEMS Capping Material
Properties of glass	Range of CTEs	✓	✓	✓
	Optical transparency		✓	✓
	Low electrical loss	✓		
	Flatness & smoothness	✓	✓	✓
	Stiffness	✓	✓	✓
	Variety of thicknesses	✓	✓	✓
	Scalability	✓	✓	✓

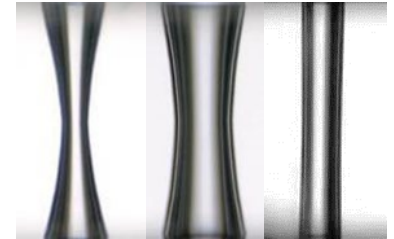
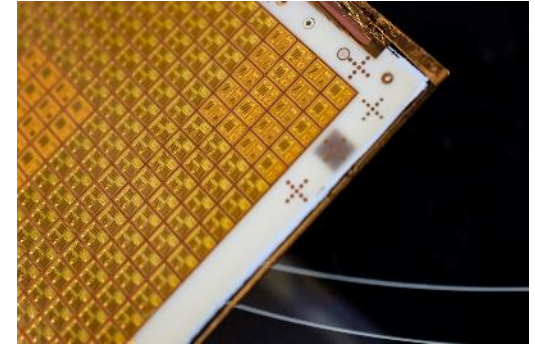
Leveraging Material Properties into Performance Advantages



Corning's Through Glass Via Capabilities

We've made significant investments in precise TGV manufacturing

TGV (Glass + Vias)	Dimensions	Wafer: up to 300mm diameter Panel: up to 500x500mm
	Thickness	100 μ m - 500 μ m
	Via Diameter	30 μ m - 100 μ m \rightarrow <30 μ m
	Pitch	Min. 2x via diameter
	Process Control	Positional accuracy: $\pm 10\mu\text{m}$ Yield: 42% improvement since '18 Metrology: 26x faster throughput since '18
	Volume	100s per month \rightarrow 1000s in '19
Metallized TGV	Process Control	90% production grade selects
	Partners	Multiple in Asia and US
	Volume	100s per month



*Precision via control at high throughput;
Capabilities to make different via shapes*

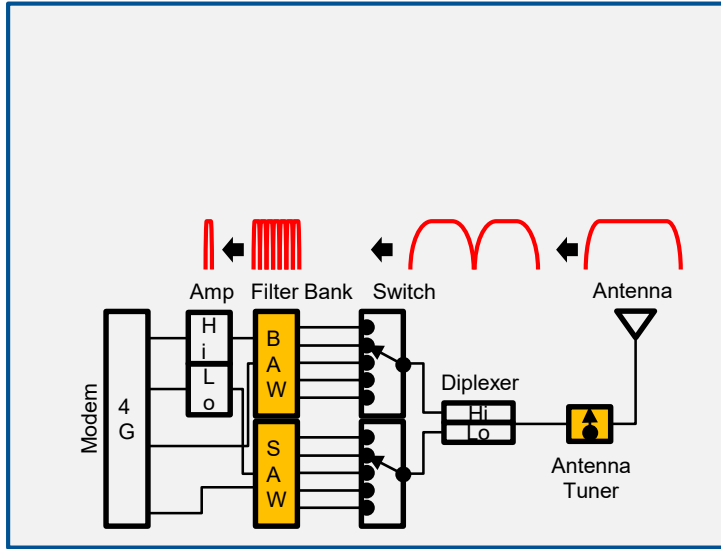
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Handset RFFE evolution

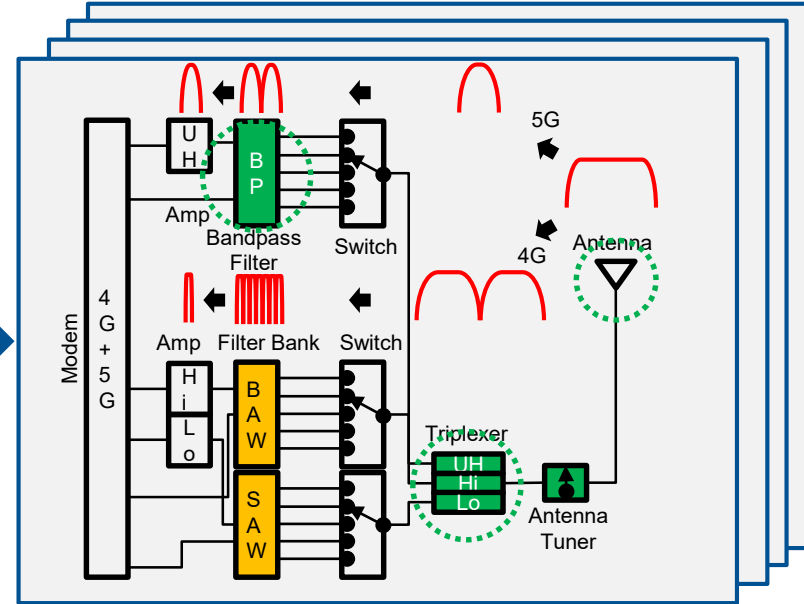
4G → 5G is increasing demand for high performance substrates like glass with precision vias

4G/LTE Module



5G

Example of 5G Module

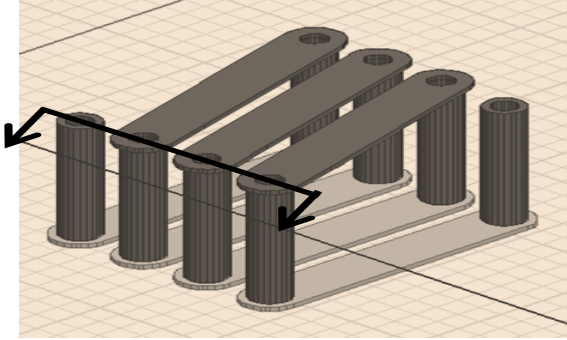


- # of Frequency bands increasing
- Usage of MIMO increasing module count
- Integration and low loss instrumental for low cost

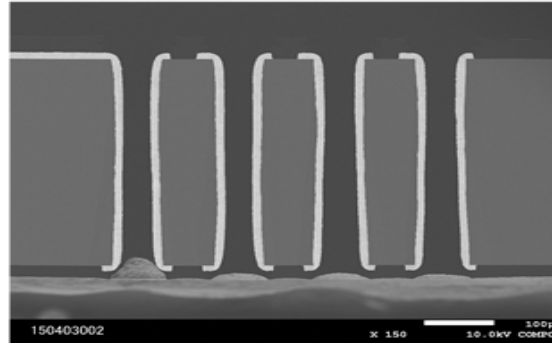
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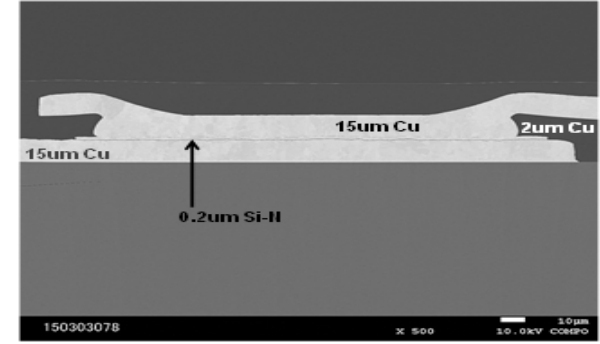
High Q inductors/capacitors have been demonstrated



3D rendering of inductor structure, top-down view

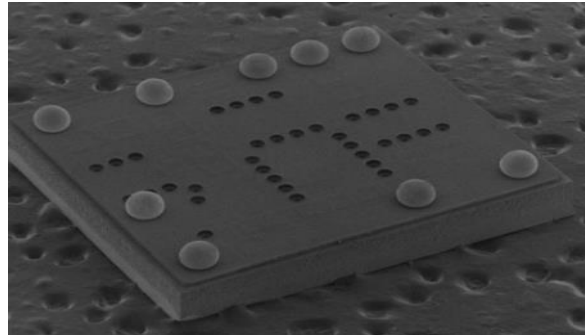


Cross-sectional SEM of a fabricated 3D inductor



Cross-sectional SEM of Cu-Silicon Nitride-Cu MIM structure

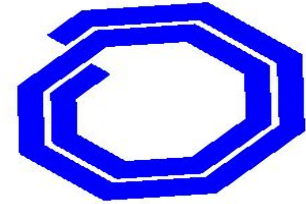
Source: Yun, Kuramochi, Shorey, "Through Glass Via (TGV) Technology for RF Applications", IMAPS 2015, Orlando, FL



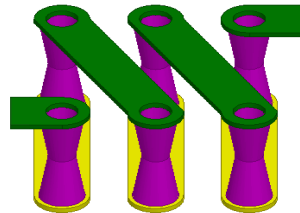
- Completed LC Network
- High Q inductance from 3D Solenoid inductor
- Capacitance achieved through MIM structure

Inductor Structures Compared

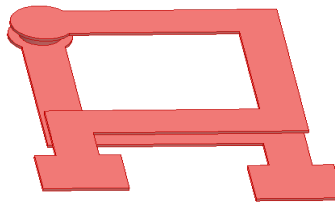
Inductor type	Inductance@ 100MHz (nH)	Q ^{PEAK} @F /GHz	Q @900MHz	SRF (GHz)
LTCC	2.0	80 @ 3.95	55	8.68
TGV (SG3)	2.0	108 @ 3.95	61	>10
2D Glass	1.9	70 @ 5.94	27	>10
HRS	1.9	64 @ 4.48	25	>10



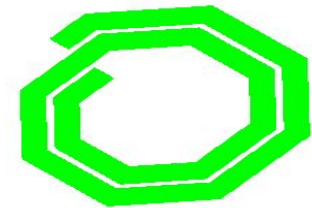
2D HRS inductor



TGV inductor



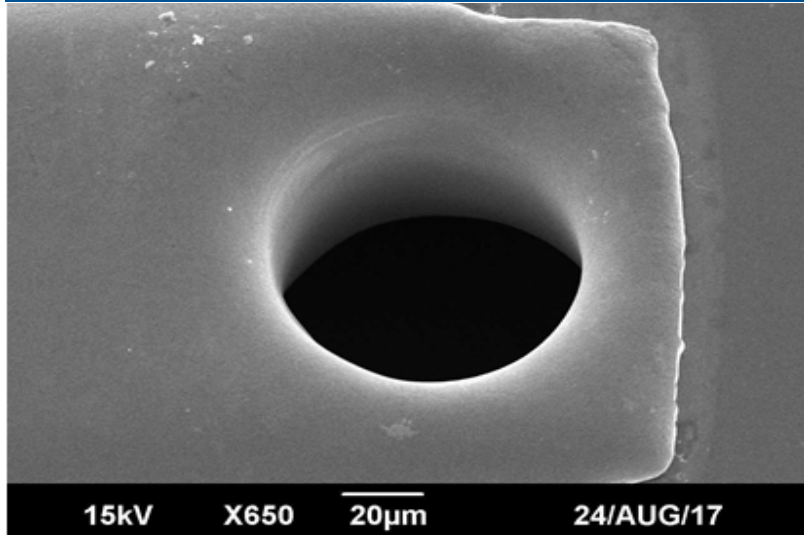
LTCC inductor



2D Glass inductor

Metal on glass achieves much smoother surfaces

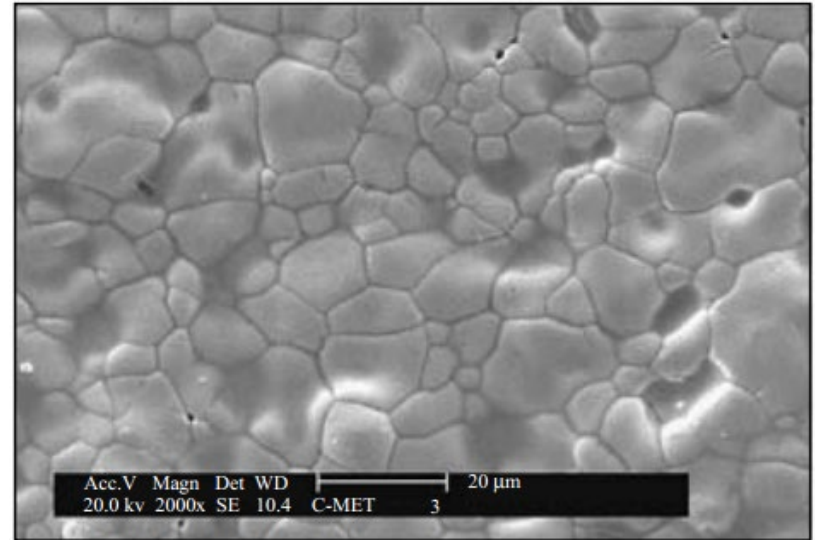
Cu on glass



Source: U. of Florida

Vs.

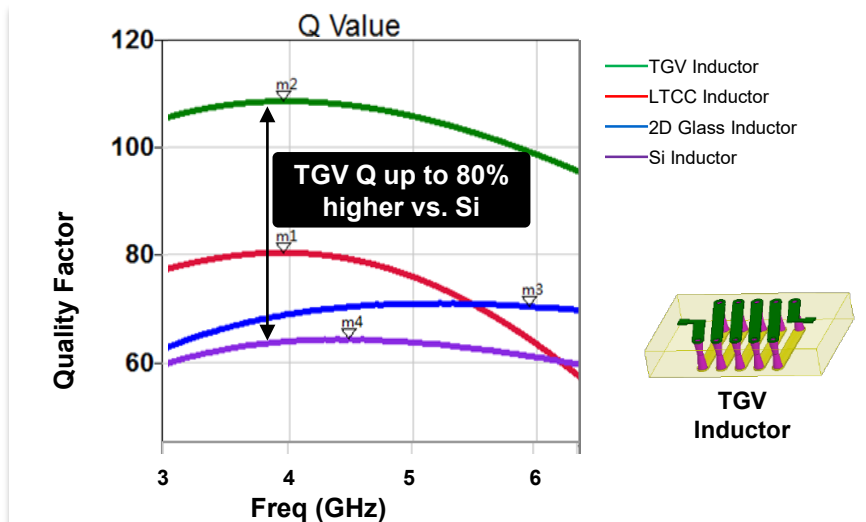
Ag on LTCC



Source: S. Rane et al Soldering and Surface Mount Technology

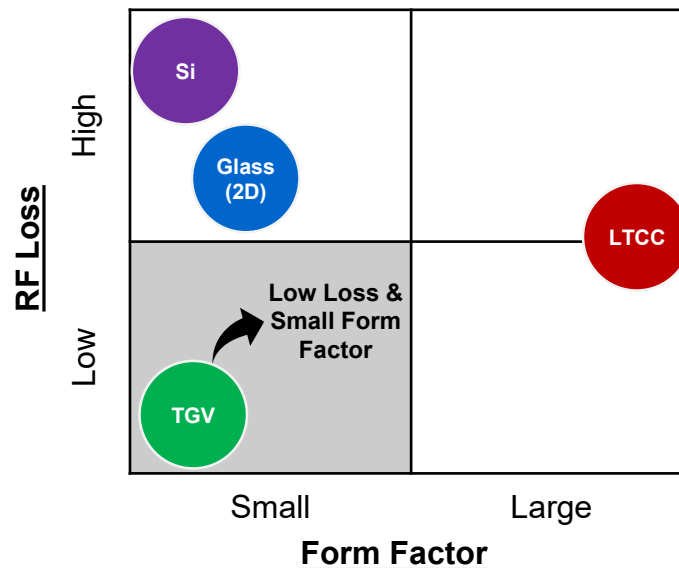
Handset RFFE evolution

TGV IPDs offer lower RF loss and small form factor



80% higher Q (lower RF loss) vs. Si

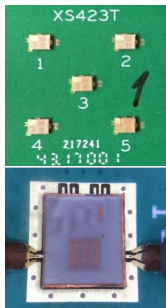
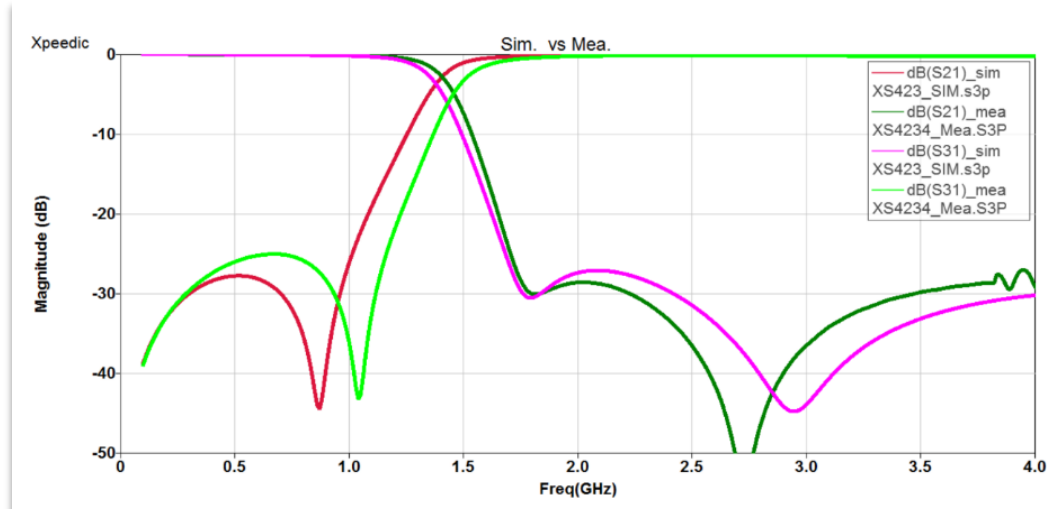
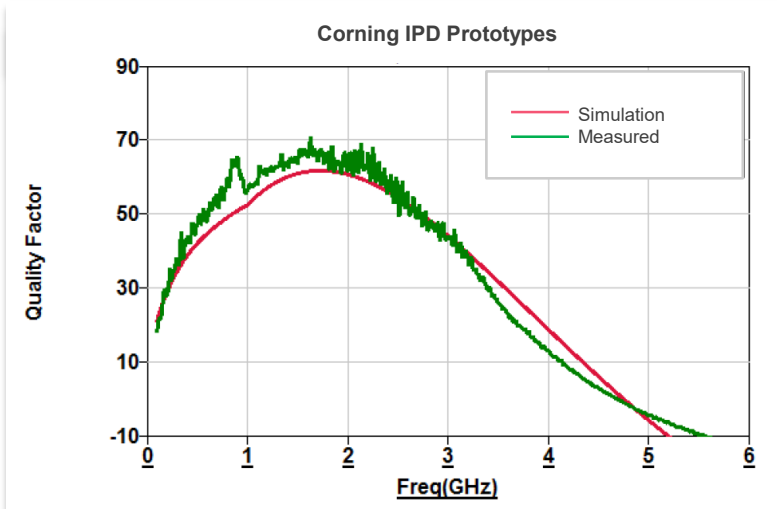
- Use same battery → Increase talk time
- Use smaller battery → Save space
- Use cheaper battery chemistry → Save up to \$2/ phone



5-10X more components, same footprint

- Smaller thinner components → slim devices

TGV IPD prototypes show high Q and low capacitance, which validates our model



- First TGV IPD prototype designed at sub-3 GHz shows higher Q (peak 70) vs. Si (peak 60) → Next, we are developing sub-7 GHz bandpass filter
- Developed knowledge in TGV and metallization processes with fab partners
- **We would like to propose jointly developing prototypes with your design**

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RF Switches

Menlo Micro introduces an ideal switch enabled by Corning TGVs

Simple & Scalable

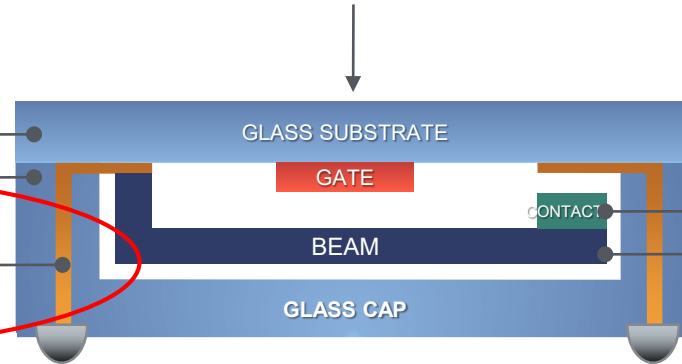
Simple design lets us go very small (50µm x 50µm unit cell)

Small → easy to scale power-handling through massive switch arrays

Small → easy to scale cost with volume : low-cost-roadmap

Unique glass packaging
Better thermals & power handling
Improved RF performance

Through Glass Via
Lower parasitics
Lower resistance
Smaller size package
Lower cost



Reliable
>3B cycles spec w/ roadmap to >20B
High-power capable

Versatile

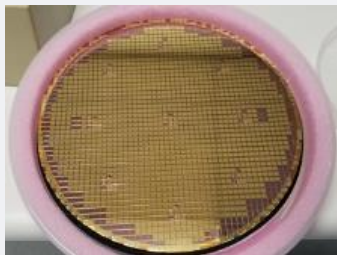
Standard PDK to create many products

Shorter design cycles → Quicker to market

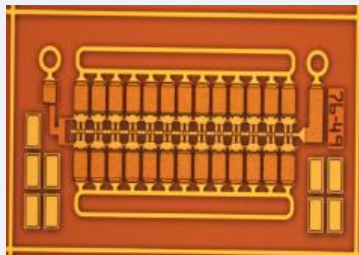
How is it made?

Revolutionary materials & processing combined with standard, scalable semiconductor manufacturing techniques

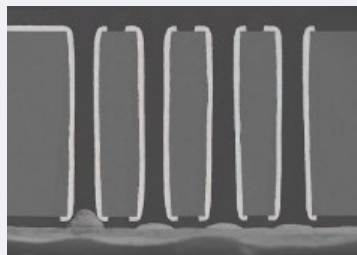
WAFER-LEVEL MANUFACTURING



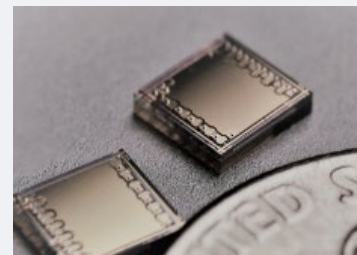
Wafer-scale manufacturing on 8 inch (200mm) glass substrates



Scalable series / parallel switch architecture: Design ability to scale up to 1000V+ and 10A+



Through Glass Vias
Miniaturized packaging with highest performance

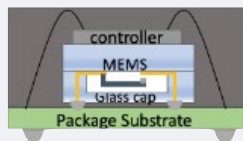


Wafer Level Chip-Scale-Package from 16mm² to < 1mm²

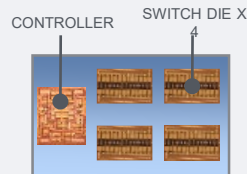
APPLICATION-SPECIFIC CONFIGURATIONS



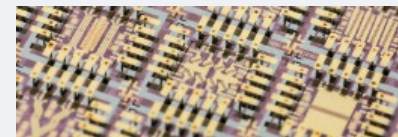
Core Switch
Simplest, smallest building block can be integrated into many power or RF applications.



Switch with integrated controls
Simpler control for high channel counts, decrease need for external components

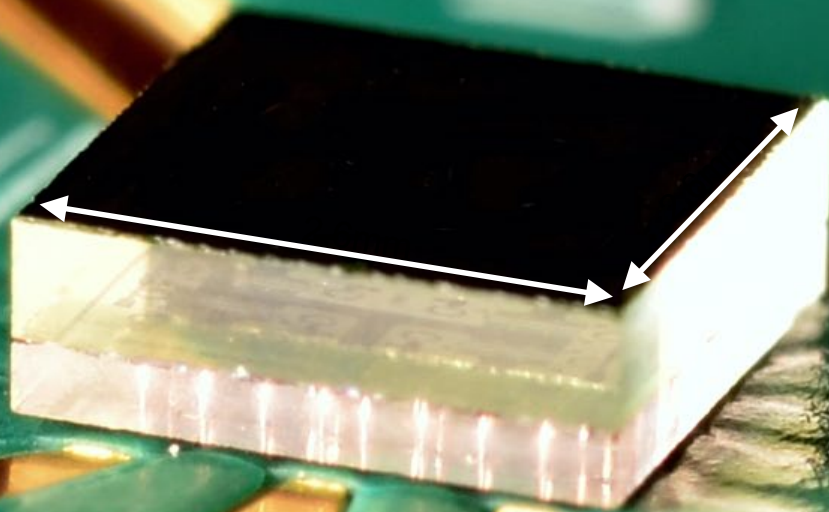
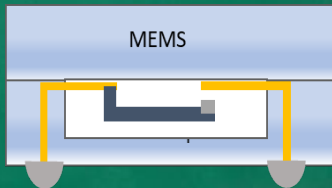


Multiple Switch die, w/routing & control: For matrix and high-density switching systems.



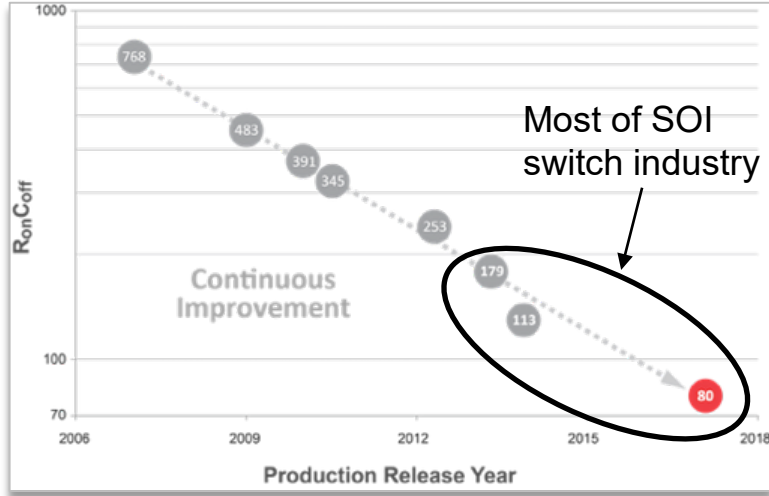
System-on-Chip
Integrate passives & other structures on die along with Menlo switches to create miniaturized RF & Power subsystems.

MM5130



RF Switches

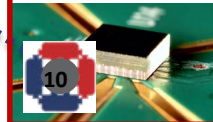
Metal-on-glass platform creates ideal RF environment for low $R_{on} \cdot C_{off}$



Source: *Peregrine Semiconductor's next-generation platform, UltraCMOS® 12 technology*

Parallel	Series	R _{On} (Ohms)	C _{Off} (ff)	S21-on @ 6Ghz (dB)	S21-off @ 6Ghz (dB)	Power Handling	Standoff Voltage
1	1	1.16	6.2	0.10	32.5	26 dBm (0.4W)	150 V
1	2	1.6	7.6	0.18	45.0	26 dBm (0.4W)	300 V
3	1	0.86	14.2	0.09	24.5	35.5 dBm (3.5W)	150 V
3	2	0.72	17.3	0.10	37.5	35.5 dBm (3.5W)	300 V
5	1	0.86	18.5	0.11	20.0	40 dBm (10W)	150 V
5	2	0.80	28.3	0.14	31.5	40 dBm (10W)	300 V

*Various DMS unit cell configs measured at RF probe



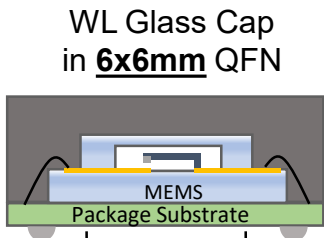
- Single series beam: $R_{on} \cdot C_{off}$ from **7 to 16fs**
- Dual series beam: $R_{on} \cdot C_{off}$ from **12 to 23fs**
- Significant margin for V_{peak} , with power handling to go beyond UE

- For SOI, $R_{on} \cdot C_{off}$ is measured for single transistor
- Actual switch product needs transistor stacks for up to 80V

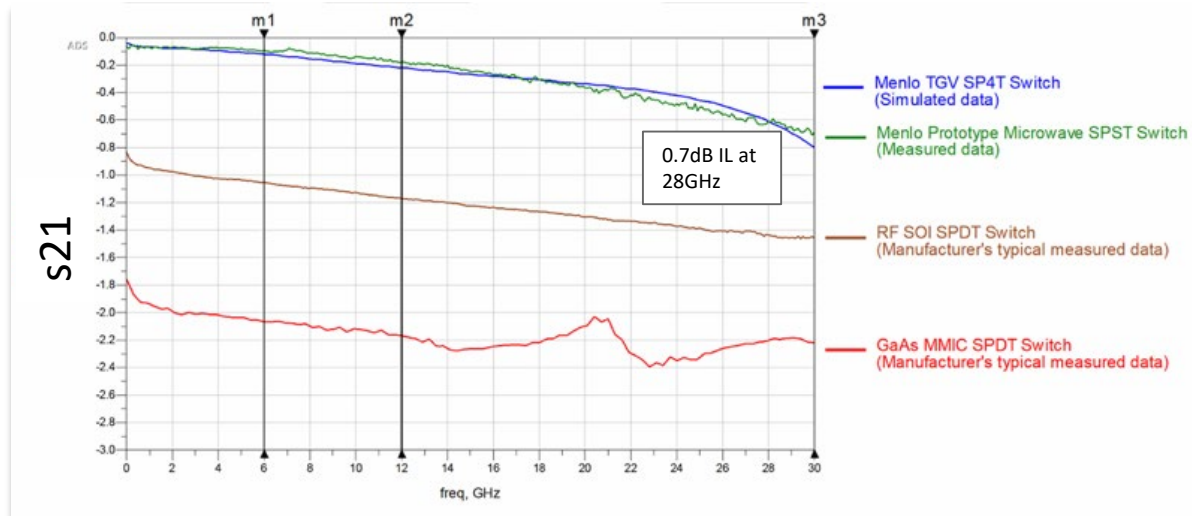
RF Switches

Lowest Losses from DC to mmWave

Previous generation of packaging (2017/2018)



Current generation of packaging



With TGV, everything gets better from an RF point of view:

- Lower parasitics, Lower bulk resistance/IL
- Improved thermals, better power handling
- Smaller size, lower cost

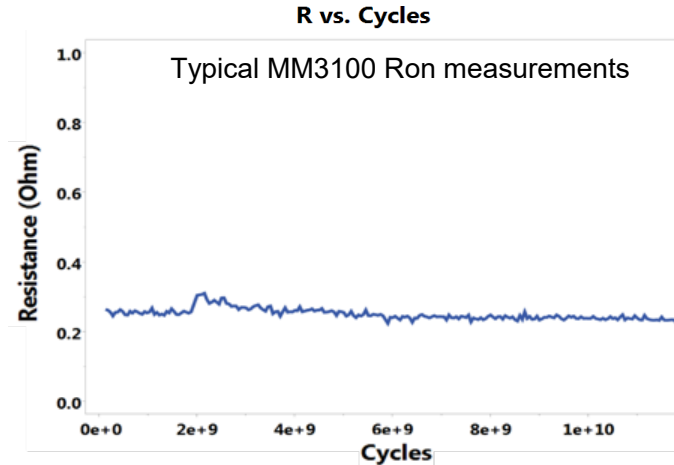
Advances in wafer-level glass packaging show mmWave performance is achievable

Reliability Testing shows Stable Operation



Continuous reliability testing for accelerated life evaluation:

- Multiple parallel automated testers capable of 20 die evaluation
- 3 ovens in operation at 3 different acceleration temperatures
- >10,000 die evaluated



Stable resistance <math><0.4\Omega</math> for over 12 billion cycles

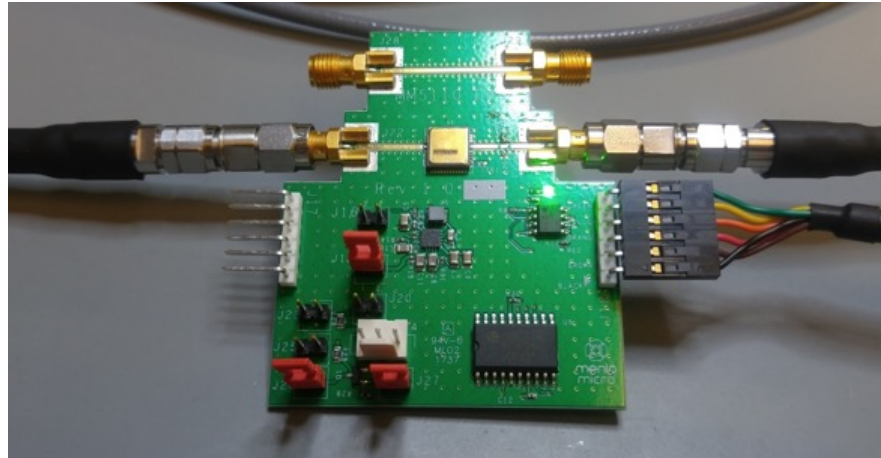
SPST 50W and 6GHz capability

- Switch ON:

Freq	S11	S21	S12	S22
1 Ghz	-32.48dB	-0.056dB	-0.061dB	-32.093dB
3 Ghz	-27.19dB	-0.100dB	-0.097dB	-33.716dB
6 Ghz	-19.00dB	-0.148dB	-0.156dB	-17.302dB

- Switch OFF:

Freq	S11	S21	S12	S22
1 Ghz	-0.058dB	-24.809dB	-24.920dB	-0.097dB
3 Ghz	-0.535dB	-16.256dB	-16.557dB	-0.644dB
6 Ghz	-0.647dB	-10.229dB	-10.634dB	-0.916dB

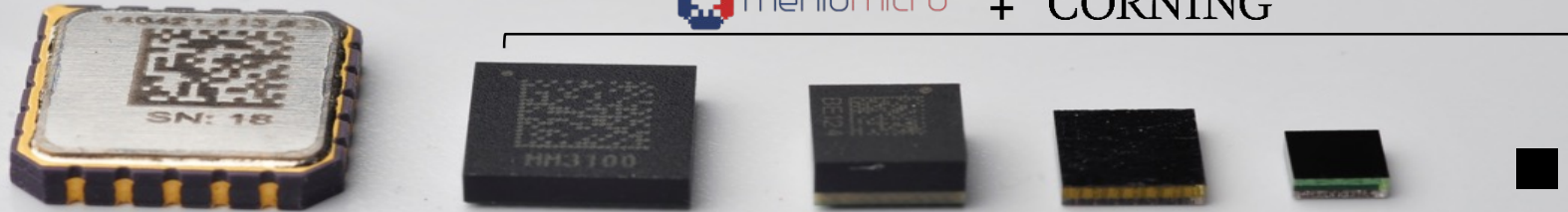


RF Switches

Packaging evolution



 **menlomicro** + **CORNING**



Corning-Menlo partnership is enabling the scale-up of a low cost TGV supply chain



RF Tunable Filter – Size, Weight & Power for RF

MM3100 Replacing PIN diodes in tunable radios



Key metrics:

- Fast frequency tuning
- Lightweight
- Long battery life
- Reliability

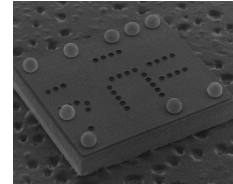
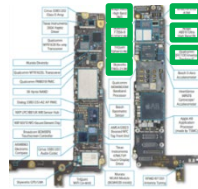
	With PIN diode 	With DMS Technology 	
	UHF interstage (2W)	UHF interstage (20W)	
Max input:	<2W	20W+	<p>Benefits:</p> <ul style="list-style-type: none"> • 10x increase in power handling • 80%+ reduction in components • 75% reduction in board size • Reduced need for thermal management, smaller box • 33% reduction in RF losses, less heat, longer battery life • 90% reduction in DC losses, less heat, longer battery life • Reduced distortion, higher data rates
Size:	200+ components (2-sided board) 50% area for complex pin-diode switch biasing	~25 components No pin-diodes, single-sided board, simple bias scheme	
Insertion Loss:	> 3dB @ 500MHz	< 2dB @ 500MHz	
Power Consumption:	1.2W	<100mW	
Linearity:	IP3: ~40dBm	IP3: ~65dBm	

Potential to enable **high power handling (5W-20W)** in a **2W** form factor

High Performance Substrate Material

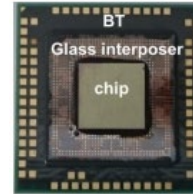
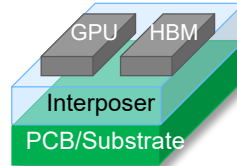
Glass and Through
Glass Vias (TGV)

Radio Frequency
Front End
(RFFE)



Filters, antennas, switches in
mobile phones, telecom
infrastructure, medical
equipment

Interposer (Photonics,
High Performance
Computing)



Re-routes connection between
layers inside a semiconductor
package (ie chip) using
through vias (ie holes)

Summary

- Material properties being leveraged into device level performance benefits
- RF Front End Usage cases broadening into Filters, Switches, Antennas
- RF MEMS Switches gain on:
 - Insertion Loss
 - Figure of Merit
 - Harmonics
 - Packaging Footprint
 - Thermal/Power Handling

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